

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

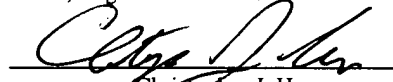
Applicant(s): Eui-Youl Ryu, et al.
Filing Date: Herewith
Title: METHOD OF MANUFACTURING SPLIT GATE FLASH MEMORY
DEVICE

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.10

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7-31-03

Date


Christopher J. Hren

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☐ under 37 CFR 1.129(a), or
(First/Second submission after Final Rejection)
- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application other than a CPA under §1.53(d); or within three months of date of entry of the national stage in international application; or before mailing date of first Office Action on the merits; or before mailing date of first Office Action after filing RCE under §1.114, whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either:
- ☐ a Statement under 37 CFR 1.97(e), as checked below, or
- ☐ the \$180.00 fee under 37 CFR 1.17(p), or
(After the 37 CFR 1.97(b) time period, but before final action or notice of allowance, or other action which closes prosecution, whichever occurs first)
- ☐ under 37 CFR 1.97(d) together with:
- ☐ a Statement under 37 CFR 1.97(e), as checked below, and
- ☐ the \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

Applicant(s): Eui-Youl Ryu

Statement Under 37 1.97(e)

- ☐ Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.
- ☐ No item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, after making reasonable inquiry, was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Enclosed herewith is form PTO-1449:

- ☒ Copies of cited references are enclosed.
- ☐ Copies of cited references are enclosed except those entered in prior application, U.S. Serial No. _____, and references that are not required to be submitted under 37 CFR 1.98.
- ☐ The listed references were cited in a counterpart foreign application.

Concise Explanation Requirement (non-English references):

- ☐ The "concise explanation" requirement under 36 CFR 1.98(a)(3)(i) for reference XX is satisfied by:
 - ☐ the explanation provided on the attached sheet.
 - ☐ the explanation provided in the Specification.
 - ☐ abstract.

It is requested the information disclosed herein be made of record in this application.

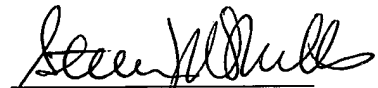
Applicant(s): Eui-Youl Ryu

Method of payment:

- ☐ Enclosed is a check in the amount of _____.
- ☐ Please charge Deposit Account of 50-1798 in the amount of _____.
- ☐ Two duplicate copies of this Statement are enclosed.
- ☒ Please charge any deficiency in fees and credit any overpayment to Deposit Account No. 50-1798.

Respectfully submitted,

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Attorney for Applicants

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Subst. Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) SAM-0407		Application Number	
				Applicant Eui-Youl Ryu, et al.			
				Filing Date Herewith		Group Art Unit	
U. S. Patent Documents							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES NO
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AR	Mih, R., "0.18um Modular Triple Self-Aligned Embedded Split-Gate Flash Memory," 2000 Symposium on VLSI Technology Digest of Technical Papers, pp. 120-121.					
	AS						
	AT						
EXAMINER					DATE CONSIDERED		
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy with next communication to applicant.							